

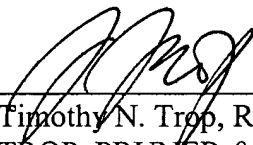
REMARKS

Claim 1 has been amended to include the subject matter of former dependent claim 3. That claim was rejected under Section 102 or Section 103 over a single reference to Giovannetti. However, Giovannetti does not teach a two-stage response to temperature. In the two-stage response set forth in the claims, at a first temperature, lower than a second temperature, the operation of the device is adjusted and then at the second higher temperature, the cache is changed from write-back to write-through. None of the material cited supports this two-stage operation. All that Giovannetti talks about is transitioning from write-back to delayed write.

Therefore, reconsideration is respectfully requested.

Respectfully submitted,

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